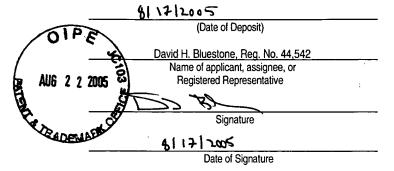
CJ6

I hereby certify that this correspondence is being deposited with the United States Postal Service, with sufficient postage, as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on



Our File No. 10808-33

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re A	Application of:)
	Sasan Cyrusian))
Patent No.: 6,737,919 B2) Examiner: Michael B. Shingleton
Issue Date: May 18, 2004) Group Art Unit: 2817
Serial	No.: 09/865,649)
Filing Date: May 25, 2001		Certificate Aug 2 4 2005
For:	METHOD AND APPARATUS FOR CALIBRATING A GM CELL UTILIZING A REPLICA GM CELL	of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attn: Certificate of Corrections Branch

Sir:

Please issue a Certificate of Correction for the above-identified patent to correct the errors listed on the accompanying PTO Form 1050. This request is being made pursuant to 35 U.S.C. § 254, since the errors comprise minor or typographical errors by the Patent and Trademark Office.

The Commissioner is hereby authorized to charge any fees required to Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

David H. Bluestone

Registration No. 44,542 Attorney for Applicant

BRINKS HOFER GILSON & LIONE P.O. Box 10395 Chicago, Illinois 60610 (312) 321-4200

Page 1 of 2

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

6,737,919 B2

APPLICATION NO. :

09/865,649

ISSUE DATE

May 18, 2004

INVENTOR(S)

Sasan Cyrusian

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

In column 1, under "FOREIGN PATENT DOCUMENTS", insert the following.

WO	97/42704	11/1997
EPO	0 308 000 A1	9/1988
EPO	0 695 030 A1	1/1996
EPO	1 020 990 A2	7/2000

In column 1, under "OTHER PUBLICATIONS", insert the following.

-- "An Analog Front-End Signal Processor for a 64 Mbits/s PRML Hard-Disk Drive Channel," IEEE Journal of Solid-State Circuits, 29, 1994, December, No. 12.--

In the Claims

In claim 12, line 5, after "a first" delete "cascade" and substitute --cascode-- in its place.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

PATENT NO. ____

6,737,919 B2

No. of additional copies

David H. Bluestone

BRINKS HOFER GILSON & LIONE

P.O. Box 10395

Chicago, Illinois 60610

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

6,737,919 B2

Page 2 of 2

APPLICATION NO.:

09/865,649

ISSUE DATE

May 18, 2004

INVENTOR(S)

Sasan Cyrusian

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims (cont'd)

In claim 12, line 14, after "a second" delete "cascade" and substitute --cascode-- in its place.

In claim 14, line 4, after "second" delete "cascade" and substitute --cascode-- in its place.

In claim 15, line 3, after "a second" delete "cascade" and substitute --cascode-- in its place.

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